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	Litis	gation
		6.496

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Searcher Location: 4B40 /	Structure (#)	Questel/Orbit 29, 02
Date Searcher Picked Up: 10/1/02	Bibliographic	Dr.Link
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Other

Other (specify)

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6026496

February 15, 2000

Method and apparatus for generating a pulse

INVENTOR: Wright, Jeffrey P., Boise, ID; Schicht, Steven F., Austin, TX

APPL-NO: 001738 ()

FILED-DATE: December 31, 1997

GRANTED-DATE: February 15, 2000

ENGLISH-ABST:

A circuit for generating a pulse with minimal delay after receiving a trigger signal includes a passgate, a gating circuit, and a reset circuit. The passgate is enabled by control signals received at the gating circuit having a trigger signal as one of the control signals. The trigger signal is also presented as an input to the passgate. When enabled, the passgate propagates the trigger signal to an output. A predetermined time after the trigger signal appears at the passgate input, a passgate control signal is turned off, thereby preventing the trigger signal from further passing through the passgate. The reset circuit is then turned on, which pulls the signal at the output of the passgate to a reference voltage, ending the pulse. Once the pulse is generated, it can be rectified and further combined with other signals to produce signals used in other parts of the circuit.

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?us6026496/pn ** SS 2: Results 1 Search statement ?prt full nonstop legalall 1/1 PLUSPAT - (C) QUESTEL-ORBIT- image PN - US6026496 A 20000215 [US6026496] - (A) Method and apparatus for generating a pulse PA - (A) MICRON TECHNOLOGY INC (US) IN - (A) SCHICHT STEVEN F (US); WRIGHT JEFFREY P (US) AP - US173897 19971231 [1997US-0001738] PR - US173897 19971231 [1997US-0001738] - (A) G06F-001/04 IC - G11C-007/22 - H03K-005/06 PCL - ORIGINAL (O): 713500000; CROSS-REFERENCE (X): 713503000 713600000 DT - Basic CT - US5033001; US5517137; US5557225; US5729169; US5764582 STG - (A) United States patent - A circuit for generating a pulse with minimal delay after receiving a trigger signal includes a passgate, a gating circuit, and a reset circuit. The passgate is enabled by control signals received at the gating circuit having a trigger signal as one of the control signals. The trigger signal is also presented as an input to the passgate. When enabled, the passgate propagates the trigger signal to an output. A predetermined time after the trigger signal appears at the passgate input, a passgate control signal is turned off, thereby preventing the trigger signal from further passing through the passgate. The reset circuit is then turned on, which pulls the signal at the output of the passgate to a reference voltage, ending the pulse. Once the pulse is generated, it can be rectified and further combined with other signals to produce signals used in other parts of the circuit. - 2000-10 1/1 LGST - (C) LEGSTAT PN - US 6026496 [US6026496] AP - US 1738/97 19971231 [1997US-0001738] DT - US-P ACT - 19971231 US/AE-A APPLICATION DATA (PATENT) {US 1738/97 19971231 [1997US-0001738]} - 20000215 US/A PATENT - 20010828 US/CC CERTIFICATE OF CORRECTION - 20020813 US/RF REISSUE APPLICATION FILED 20020214 UP - 2002-35 1/1 CRXX - (C) CLAIMS/RRX PN - 6,026,496 A 20000215 [US6026496] PA - Micron Technology Inc ACT - 20020214 REISSUE REQUESTED ISSUE DATE OF O.G.: 20020813 REISSUE REQUEST NUMBER: 10/055514 EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2188

Reissue Patent Number:

1/2 PAST - (C) Thomson Derwent

AN - 200233-001724

PN - 6026496 A [US6026496] OG - 2002-08-13

ACT - REISSUE APPLICATION FILED

2/2 PAST - (C) Thomson Derwent

AN - 200135-000182

PN - 6026496 A [US6026496] OG - 2001-08-28

ACT - CERTIFICATE OF CORRECTION

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fam us6026496/pn
 1 Patent Groups
 ** SS 2: Results 1
                    3
 Search statement
?famstate nonstop
 1/1 INPADOC - (C) INPADOC
 PN - US 6026496 A 20000215 [US6026496]
 TI - METHOD AND APPARATUS FOR GENERATING A PULSE
 IN - WRIGHT JEFFREY P [US]; SCHICHT STEVEN F [US]
 PA - MICRON TECHNOLOGY INC [US]
 AP - US 1738/97-A 19971231 [1997US-0001738]
 PR - US 1738/97-A 19971231 [1997US-0001738]
 IC - G06F-001/04
 1/1 LEGALI - (C) LEGSTAT
 PN - US 6026496 [US6026496]
 AP - US 1738/97 19971231 [1997US-0001738]
 DT - US-P
 ACTE- 19971231 US/AE-A
       APPLICATION DATA (PATENT)
       {US 1738/97 19971231 [1997US-0001738]}
     - 20000215 US/A
      PATENT
     - 20010828 US/CC
      CERTIFICATE OF CORRECTION
     - 20020813 US/RF
       REISSUE APPLICATION FILED
      20020214
 UP - 2002-35
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